

Atty. Docket No. OF03P212/US

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JUN 22 2005

IN RE APPLICATION OF : :

Jae Suk LEE et al. : GROUP ART UNIT: 2823

OK TO ENTER

APPLICATION NO: 10/733,884 : :

FILED: DECEMBER 3, 2003 : EXAMINER: DANG, Trung

T. Dang

FOR: METHOD OF MANUFACTURING  
A SEMICONDUCTOR DEVICE

I hereby certify that this document is being facsimile transmitted to the USPTO, Mail Stop AF, or deposited with the United States Postal Service as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on June 22, 2005.

By: Jennie Heaton  
Jennie Heaton

AMENDMENT AND REQUEST FOR RECONSIDERATION UNDER 35 U.S.C. 132  
AND 37 C.F.R. 1.111

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SIR:

Responsive to the Office Action dated March 25, 2005, Applicant elects, with traverse, Group I, Claims 1 and 3-9, drawn to a method of manufacturing a semiconductor device, and (also with traverse) Group A, Claims 1 and 3-9, directed to a method that comprises etching parts of the insulating layer where a plurality of via holes will be formed to a certain thickness so as not to expose the first conductive line, and respectfully requests (i) entry of the following amendments and (ii) consideration of the following remarks.